

OPEN LOOP COMMON MODE DRIVER FOR SWITCHED CAPACITOR INPUT TO SAR

ABSTRACT OF THE DISCLOSURE

Open loop common mode driver for switched capacitor input to SAR. A unity gain driver amplifier is disclosed for driving an output node that is connected to a capacitive load. The amplifier includes a first stage amplifier for driving an intermediate node, a positive voltage input node for being connected to an input voltage and a negative input node for receiving a feedback signal. A complimentary output stage is provided having an input connected to the intermediate node and an output connected to the output node, a voltage representative of the voltage on the output node fed back to the negative input of said first stage amplifier. Isolation circuitry then isolates the output node from the negative input node of the first stage amplifier as to phase shift due to large values of the capacitive loading during operation of the driver amplifier.